Attorney's Docket No.: 12732-212001 / US7008

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hajime Kimura Art Unit: Unknown Serial No.: New Application Examiner: Unknown

Filed: February 27, 2004

Title : SEMICONDUCTOR DEVICE AND DRIVING METHOD THEREOF

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449.

Applicant would like to bring the Examiner's attention to U.S. Patent Application Nos. 10/720,847 (identified as Desig. ID "AR"), 10/743,347 (identified as Desig. ID "AS"), and 10/230,068 (identified as Desig. ID "AC"). These applications may be related to the instant application.

Submitted herewith is an English translation of the following foreign language references, or portions thereof:

Desig. ID	Source			
AF	Japan 2001-005426: English Abstract – DIALOG(R) File 347:JAPIO			
	(2002); JPO & JAPIO; Accession No. 06777951 and family to U.S.			
	Patent Publication No. 2002/0153844 A1 (Desig. ID "AA")			
AG	Japan 2002-514320: English Abstract – DIALOG(R) File 352:Derwent			
	WPI (2003); Accession No. 012166998 and family to U.S. Patent No.			
	6,229,506 B1 (Desig. ID "AD")			
AH	Japan 2002-517806: English Abstract – DIALOG(R) File 352:Derwent			
	WPI (2003); Accession No. 012934103 and family to U.S. Patent No.			
	6,373,454 B1 (Desig. ID "AE")			
AK	International Publication No. WO 01/006484: English Abstract –			
	DIALOG(R) File 352:Derwent WPI (2004); Accession No.			
	013654281 and family to European Patent No. 1 130 565 A1 (Desig.			
	ID "AJ")			
AL	International Publication No. WO 02/039420: English Abstract and			
	family to U.S. Patent Publication No. 2003/128200 A1 (Desig. ID			
	"AB")			
AM	International Publication No. WO 03/038793: Full Translation			
AN	International Publication No. WO 03/038794: Full Translation			
AO	International Publication No. WO 03/038795: Full Translation			
AP	International Publication No. WO 03/038796: Full Translation			
AQ	International Publication No. WO 03/038797: Full Translation			

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Serial No.: New Application Filed: February 27, 2004

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This statement is being filed with the application. Accordingly, only copies of foreign patent documents and non-patent literature are enclosed. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: February 27, 2004

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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 12732-212001	Application No. New Application
by A _l	closure Statement pplicant	Applicant Hajime Kimura	
(Use several sheets if necessary) (37 CFR §1.98(b))		Filing Date February 27, 2004	Group Art Unit Unknown

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	2002/0153844 A1	10/24/2002	Koyama			03/11/2002
	AB	2003/0128200 A1	07/10/2003	Yumoto		_	11/07/2001
	AC	2003/0062524 A1	04/03/2003	Kimura			08/29/2002
	AD	6,229,506 B1	05/08/2001	Dawson et al.			04/22/1998
	AE	6,373,454 B1	04/16/2002	Knapp et al.			06/09/1999

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner	Desig.	Document	Publication	Country or			Translation	
Initial	ID	Number	Date	Patent Office	Class	Subclass	Yes	No
	AF	2001-005426	01/12/2001	JAPAN			ABS	
	AG	2002-514320	05/14/2002	JAPAN			ABS	
	AH	2002-517806	06/18/2002	JAPAN			ABS	
	AI	EP 1 103 946 A2	05/30/2001	EUROPE			In Engl	ish
	AJ	EP 1 130 565 A1	09/05/2001	EUROPE			In English	
	AK	WO 01/006484	01/25/2001	WIPO			ABS	
	AL	WO 02/039420	05/16/2002	WIPO			ABS	
	AM	WO 03/038793 A1	05/08/2003	WIPO			Full	
	AN	WO 03/038794 A1	05/08/2003	WIPO			Full	
	AO	WO 03/038795 A1	05/08/2003	WIPO			Full	_
	AP	WO 03/038796 A1	05/08/2003	WIPO			Full	
	AQ	WO 03/038797 A1	05/08/2003	WIPO			Full	

Other Documents (include Author, Title, Date, and Place of Publication)				
Examiner	Desig.			
Initial	ID	Document		
ΔD	AR	Hajime Kimura; "Current Driving Circuit and Display Device Using The Current Driving Circuit";		
		U.S. Patent Application No. 10/720,847; Filed November 25, 2003		
AS	ΔS	Hajime Kimura; "Semiconductor Device and Display Device Utilizing the Same"; U.S. Patent		
		Application No. 10/743,347; Filed December 23, 2003		

Examiner Signature	Date Considered				
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with					
next communication to applicant.					